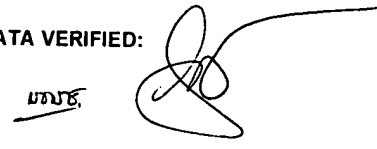
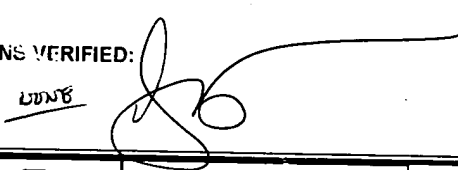
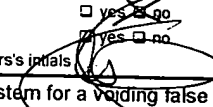


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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10025289	FILING DATE 12/19/2001	CLASS 716 762	SUBCLASS 5	GAU 2857	EXAMINER Thompson
**APPLICANTS: Martin Andrew; Krishnamurthy Narayanan;					
**CONTINUING DATA VERIFIED: 					
**FOREIGN APPLICATIONS VERIFIED: 					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>					
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Verified and Acknowledged Examiners's initials 				ATTORNEY DOCKET NO SC11522TS	
TITLE : Design verification system for a voiding false failures and method therefor					

U.S. DEPT. OF COM. / PAT. & TM. PTO-436 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
ISSUE FEE		Assistant Examiner	
Amount Due	Date Paid	Total Claims	
<input type="checkbox"/> TERMINAL DISCLAIMER		Print Claim for O.G.	
		DRAWINGS	
		Sheets Draw.	Figs. Draw.
		Print Fig.	
		Primary Examiner	
		Application Examiner	
		PREPARED FOR ISSUE	
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